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Embedded two level direct adaptive fuzzy controller for DC motor speed control

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KEYWORDS

Embedded systems; Direct adaptive fuzzy controller; DC motor; Speed control; T–S fuzzy controller **Abstract** This paper presents a proposed approach based on an adaptive fuzzy logic controller for precise control of the DC motor speed. In this concern, the proposed Direct Adaptive Fuzzy Logic Controller (DAFLC) is estimated from two levels, where the lower level uses a Mamdani fuzzy controller and the upper level is an inverse model based on a Takagi–Sugeno (T–S) method in which its output is used to adapt the parameters of the fuzzy controller in the lower level. The proposed controller is implemented using an Arduino DUE kit. From the practical results, it is proved that the proposed adaptive controller improves, successfully both the performance response and the disturbance due to the load in the speed control of the DC motor.

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1. Introduction

The permanent magnet direct current (PMDC) motor is an example of electromechanical systems with electrical and mechanical components. In this concern, this type of motors is commonly used in many industrial applications such as robot manipulators, home applications and sun trackers. Not-

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ing that, there are many classical and intelligent control techniques [1-5], such as PID, FLC, artificial neural networks, and many methods of AFLC are applied to control the speed of DC motor for achieving high performance. Classical mathematics and conventional control theory are very limited and difficult in modeling and controlling complex nonlinear dynamical systems [6,7]. On the other hand, fuzzy logic controller (FLC) is an alternative tool for PID controller [8,9], where the motivation for using fuzzy logic technology in control systems stems from the fact that it allows control designers to build a controller even when their understanding of the system is still in a vague and incomplete [10,11]. It provides a good tool for the control of nonlinear systems that are difficult in modeling [12,13]. But, the design of the FLC is not the optimum, where numerous difficulties appear to choose the controller parameters. Also, the presence of noise or any

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Please cite this article in press as: Zaki AM et al., Embedded two level direct adaptive fuzzy controller for DC motor speed control, Ain Shams Eng J (2015), http://dx. doi.org/10.1016/j.asej.2015.10.003 changes in the plant parameters, the controller may not be able to achieve adequate performance level. Finally, an adaptive controller is one of the controllers that adjust itself to reach adequate performance [14–16]. In this concern, the main contribution of the present study is the success of the proposed controller to be implemented on the Arduino DUE board to control the speed of DC motor using a proposed DAFLC which uses Takagi–Sugeno (T–S) fuzzy system in the second level to minimize the effects of the system's load disturbance.

In the present paper, two-level fuzzy controller (TLFC) is presented to control and improve the speed of DC motor. In general, it is considered as an adaptive fuzzy control, where DAFLC can be adjusted directly with a reference model. This design is implemented on Arduino DUE kit, applying Arduino 1.5.5-r2 software. Finally, the organization of the paper is as follows: Section 2 describes the structure and the design of the fuzzy PI controller, and the DAFLC is explained in Section 3. The Arduino implementation is explained in Section 4. Results and discussions are presented in Section 5. Finally, conclusion is in the last section.

2. Fuzzy PI controller design

FLC enables control designers to design and build the controller by forming IF-THEN rules which are in the form of statements [12,17]. The structure of FLC contains four main parts [14,15] as shown in Fig. 1: Fuzzification, inference mechanism, rule base and defuzzification, where fuzzification part is used for converting real input to fuzzy input. The rule-base part contains the expert knowledge in the form of a set of rules. The inference mechanism part evaluates which control rules are relevant at the current time and then decides what the input to process should be and defuzzification part is opposite fuzzification, where it is used for converting fuzzy output to real output. There are two inputs and one output, where the first input is error (e) which is the difference between reference speed of the motor (W_{ref}) and its actual speed of motor (W_{act}) . The second input is the change of error (Ce) which is defined as the difference between the present error e(k) and the previous error e(k-1) given by Eqs. (1) and (2), respectively, where, k is the sampling instance. On the other hand, the output is the change of control signal (ΔU). For getting control signal (u), Eq. (3) is applied, as shown in Fig. 2 as follows:

$$e(k) = W_{\rm ref} - W_{\rm act} \tag{1}$$

$$Ce(k) = e(k) - e(k-1)$$
 (2)

$$u(k) = u(k-1) + \Delta U(k) \tag{3}$$

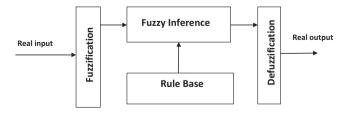


Figure 1 Structure of fuzzy logic controller.

For each of the two inputs and output, there are seven fuzzy sets on universes with linguistic values; namely: Negative Big (NB), Negative Medium (NM), Negative Small (NS), Zero (Z), Positive Small (PS), Positive Medium (PM) and Positive Big (PB). The motor range of speed is very large, so the error and change of error ranges are very high, as it is difficult to handle with large values. The universe of discourse for input and output is normalized value from -6 to 6, where we can use gains G1, G2 and G3 for two inputs and output universe of discourse respectively [18] as shown in Fig. 2. The designers can choose many different shapes based on their preference and experience [17]. These are characterized by the Gaussian membership and are shown in Fig. 3 where, in general the mathematical expression for Gaussian function is

$$\mu(x) = \exp\left(-\frac{1}{2}\left(\frac{x-c}{\sigma}\right)^2\right) \tag{4}$$

where

c: the center of membership function and

 $\sigma > 0$: determines the spread or width of the function.

Mamdani type rule base structure is used in this controller; there are 49 rule bases, as given in Table 1. To evaluate the value of the rule antecedents, one should use the AND operator (minimum). But, if a given fuzzy rule has multiple consequent, the OR fuzzy operator (maximum) is used to obtain a single number that represents the result of the consequent evaluation [19]. Finally, many defuzzification methods can be used for leading defuzzification [20]. Finally, weighted average method (Eq. (5)), is proposed to be applied throughout the present work. The matter is due to that it is characterized by its simple calculations and easy for implementation on Arduino (kit/board). From which, and after compensation of the ΔU value into Eq. (3), one can easily get the control signal u (duty cycle). Hence, this result is transferred to the DC motor.

$$\Delta U = \frac{\sum_{i}^{n} \mu_{i}(\Delta U_{i}) \cdot c_{i}}{\sum_{i}^{n} \mu_{i}(\Delta U_{i})}$$
(5)

where

 $\mu_i(\Delta U_i)$: values of membership function (MF) for output, and

 c_i : values of output MF centers.

3. Direct Adaptive Fuzzy Logic Controller (DAFLC)

DAFLC is called a fuzzy model reference learning controller (FMRLC). The functional block diagram for the FMRLC [14,21] is shown in Fig. 4. Basically, it consists of four main parts: the plant, fuzzy controller to be tuned, the reference model, and the learning mechanism block (an adaptation mechanism). Mainly, the fuzzy control system loop (the lower level of Fig. 4) operates to make " $W_{act}(kT)$ " track " $W_{ref}(kT)$ " by manipulating u(kT), while the upper level adaptation control loop which uses Takagi–Sugeno (T–S) fuzzy controller as an inverse model (the upper level of Fig. 4) seeks to make the output of the plant $W_{act}(kT)$ track the output of the reference model "Wm(kT)" by manipulating the fuzzy controller to Fig. 4) seeks to make the output of the plant $W_{act}(kT)$ track the output of the reference model "Wm(kT)" by manipulating the fuzzy controller parameters. FLC is explained in Section 2 and we will describe the other parts of DAFLC.

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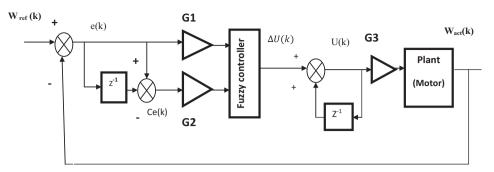


Figure 2 Block diagram of fuzzy PI control system.

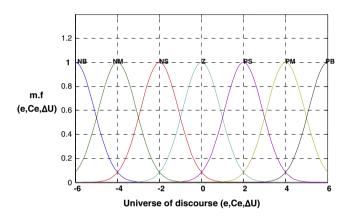


Figure 3 Membership functions for error (*e*), change in error (Ce) and change in control signal (ΔU).

3.1. The reference model

One of the priorities is to choose from the reference model that specifies the desired performance for the overall system. It may be discrete or continuous time, linear or nonlinear, time-invariant or time-varying, and so on. The second order dynamical reference model was used to specify the desired performance for DC motor. The continuous time and discrete time models are given in Eqs. (6) and (7), respectively:

$$G(s) = \frac{\omega_n^2}{s^2 + 2\zeta\omega_n s + \omega_n^2} \tag{6}$$

$$Wm(k) = d1m * Wm(k-1) - d2m * Wm(k-2) + d3m * W_{ref}(k)$$
(7)

| Table 1 Rule base. | | | | | | | |
|----------------------|----|----|----|----|----|----|----|
| е | NB | NM | NS | Z | PS | РМ | PB |
| Ce | | | | | | | |
| NB | NB | NB | NB | NB | NM | NS | Ζ |
| NM | NB | NB | NB | NM | NS | Ζ | PS |
| NS | NB | NB | NM | NS | Ζ | PS | PM |
| Z | NB | NM | NS | Z | PS | PM | PB |
| PS | NM | NS | Z | PS | PM | PB | PB |
| PM | NS | Ζ | PS | PM | PB | PB | PB |
| PB | Z | PS | PM | PB | PB | PB | PB |

where

 ω_n : the natural frequency,

 ζ : damping ratio,

d1m, d2m and d3m: discrete model parameters and functions in ω_n and ζ .

3.2. Learning mechanism

The FLC parameters are tuned by the learning mechanism that consists of two main parts: a "fuzzy inverse model" and a "knowledge-base modifier". The fuzzy inverse model is proposed as T–S fuzzy controller, which is called a "functional fuzzy system" where, the consequent part is just a mathematical function of the input variables [22–24]. The general format of the rule was given in Eq. (8):

$$Ri: IF B_{i1}(x_1), B_{i2}(x_2), \dots, B_{in}(x_n) THEN$$

$$P = f(x_1, x_2, \dots, x_n).$$
(8)

where

 x_1, x_2, \dots, x_n : input variables, $B_{ii}(x_i)$ $(j = 1, 2, \dots, n)$: a fuzzy set on X_i .

In this concern, the antecedent part is processed in exactly the same way as the Mamdani method, and then the consequent part is processed where, P is calculated as the function of real inputs [18]:

$$P = a_0 + a_1 * x_1 + a_2 * x_2 + \dots + a_n * x_n \tag{9}$$

In this paper the T–S fuzzy inverse model has two inputs $(x_1 \text{ and } x_2)$, 25 rule base and one output (*P* is a function of two real inputs), where, each of the two inputs membership is triangular, and there are five fuzzy sets on universes with linguistic values; namely: Negative (N), Negative Small (NS), Zero (*Z*), Positive Small (PS) and Positive (P). The first input (x_1) is error (em(*k*)) which is the difference between the output speed of the motor, $W_{act}(k)$, and model output speed of the motor (Wm(*k*)). The second input (x_2) is change of error (Cem(*k*)) which is defined as the difference between the present error em(*k*) and the previous error em(*k* – 1) and its output is P(k) (adaptation factor) which leads em(*k*) to be zero. The proposed algorithm can be summarized as follows:

- Initialize the FLC parameters.
- Calculate the control signal value (u(k)), as illustrated in Eq. (3).

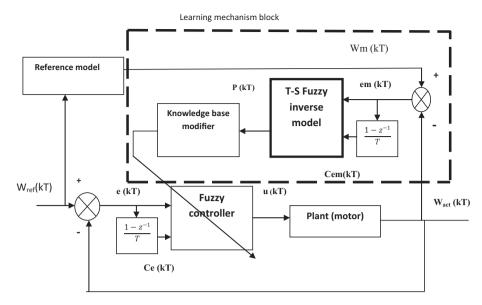


Figure 4 Block diagram of Direct Adaptive Fuzzy Logic Controller (DAFLC).

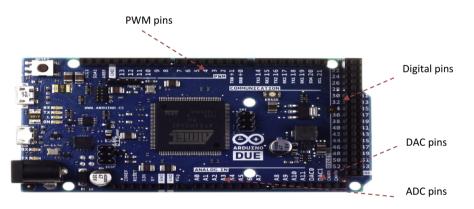


Figure 5 Arduino DUE kit.

- Measure the speed value $(W_{act}(k))$ and calculate the two inputs of the T–S fuzzy inverse model (error (em(k))) and change of error (Cem(k)).
- Calculate the adaptation factor (P(k)).
- Find all the rules in the fuzzy controller who the "active set" of the rules at time kT T.

$$\mu_i(e(kT - T), \ \operatorname{Ce}(kT - T)) > \varepsilon \tag{10}$$

where $0 \le \varepsilon < 1$, because Gaussian membership functions (MFs) are used in FLC and we will not have to modify all the output centers at each time step.

| Table 2 Parameters of DC motor. | | | | | |
|---|------------|--------|-------------------|--|--|
| Motor characteristics | Parameters | Values | Units | | |
| Terminal resistance | R_a | 2 | Ω | | |
| Terminal inductance | L_a | 1.1 | mH | | |
| Viscous friction | B_m | 0.2 | N m s/rad | | |
| Rotor inertia | J_m | 0.02 | kg m ² | | |
| Back EMF constant | K_e | 0.1 | V/rad/s | | |
| Torque constant | K_m | 0.1 | N m / A | | |

• The knowledge-base modifier adjusts the centers and the gains of the output MFs of the FLC as illustrated in Eqs. (11) and (12) where, C_i (kT) denote the center of the *i*th output membership function at time kT and G_j (kT) denote the gains of the two inputs and output where, j = 1, 2, 3.

$$C_i(k) = C_i(k-1) + P(k)$$
(11)

$$G_j(k) = G_j(k-1) + P(k)$$
(12)

4. Arduino implementation

Arduino [25] is an open-source electronics platform based on easy-to-use hardware and software. The Arduino Due is a microcontroller board based on the Atmel SAM3X8E ARM Cortex-M3 CPU Fig. 5, and it is the first Arduino board based on a 32-bit ARM core microcontroller (that allows operations on 4 bytes wide data within a single CPU clock). It has 54 digital input/output pins (of which 12 can be used as PWM outputs), 12 analog inputs, 4 UARTs (hardware serial ports), a

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Figure 6 Real system of the DC motor speed control using Arduino DUE.

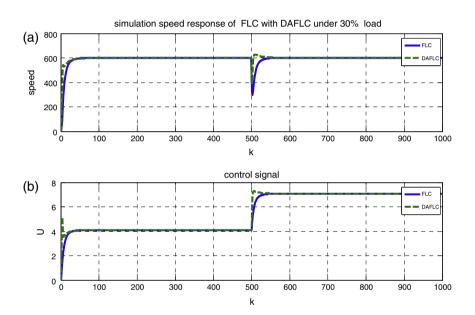


Figure 7 (a) Simulation speed response of FLC with DAFLC under 30% load and (b) control signals.

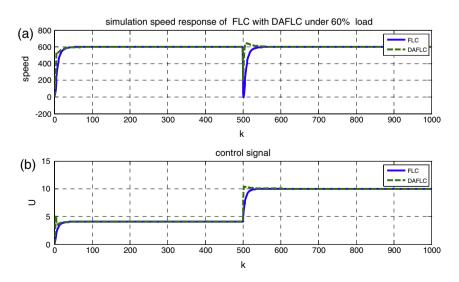
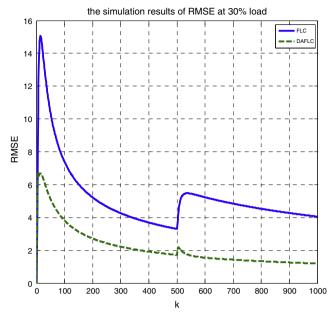


Figure 8 (a) Simulation speed response of FLC with DAFLC under 60% load and (b) control signals.

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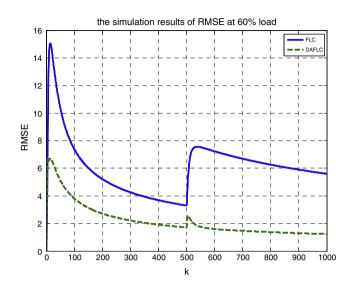


Figure 10 Simulation of RMSE for FLC and DAFLC under 60% load.

Figure 9 Simulation of RMSE for FLC and DAFLC under 30% load.

84 MHz clock, a USB capable connection, 2 DAC (digital to analog), 96 kBytes of SRAM and 512 kBytes of Flash memory for the program code. In the proposed Arduino-based speed control system, FLC and DAFLC will be implemented applying Arduino hardware using Arduino 1.5.5-r2 software code. In this concern, Gaussian fuzzifier, Gaussian MF, max of min inference rule and Weighted average method defuzzifier method, will be investigated. Finally, the transfer function of the reference model is chosen by a second order system with the natural frequency of 2.1 rad/s and the damping ratio of

1. The parameters of the discrete equation (Eq. (7)) are given as follows: d1m = 1.2269, d2m = 0.3763, and d3m = 0.1494. Considering the motor specification is illustrated in Table 2 where, the motor current can be measured by ACS712 which has sensitivity of 66 mV/A. The structure of the laboratory experiment is limited to the maximum 60% load. Its speed is measured by optical encoder sharp J3 GPIA30R, which is considered as a digital input of the Arduino (Fig. 5). Hence, this value was incorporated into the controller algorithm (FLC or DAFLC), resulting-in the control signal u(k) in the form of PWM. Following that, it is sent to the drive circuit MD10C where, one of the features of MD10C is supporting

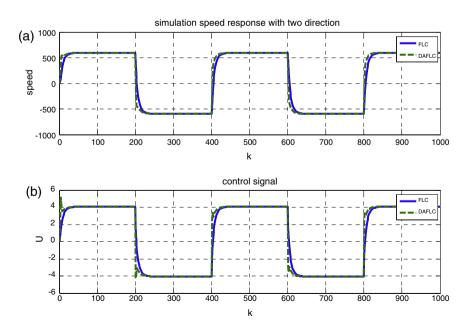


Figure 11 (a) Simulation speed response with two directions and (b) control signal.

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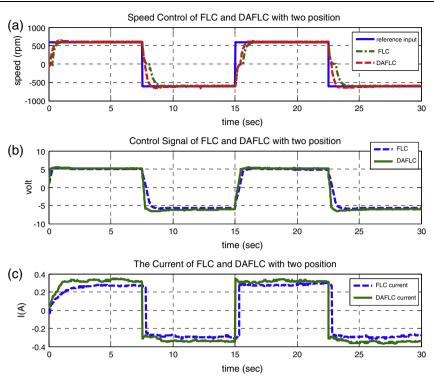


Figure 12 (a) Step response of the DC motor for DAFLC with two positions, (b) control signal and (c) current waveform.

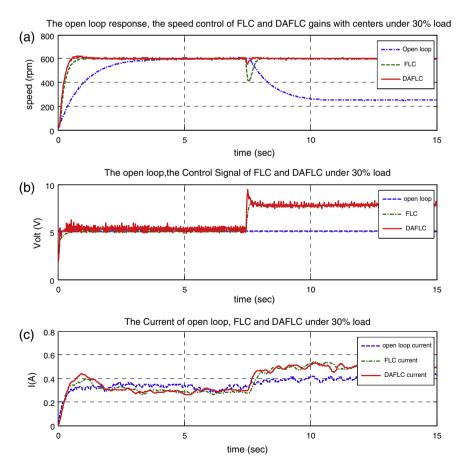


Figure 13 (a) Open loop response of the DC motor, FLC with DAFLC response under 30% load, (b) control signals and (c) current waveform.

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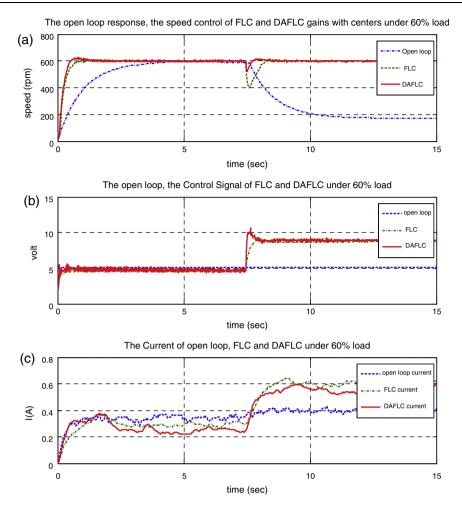


Figure 14 (a) Open loop response of the DC motor, FLC and DAFLC response under 60% load, (b) control signals and (c) current waveform.

both the sign-magnitude and locked anti-phase PWM signal, the matter which means that one can control motor in two different ways [26]. Real system of the DC motor speed control using Arduino DUE is shown in Fig. 6.

5. Results and discussion

The proposed controller is implemented in Arduino DUE for practical speed controller of the DC motor. The dynamic performance of the DC motor for FLC and DAFLC is applied. In order to clear the improvement of the proposed DAFLC controller, the FLC controller also is implemented for comparison purposes using the same number of the membership functions, a number of rules, the same universe of discourse and the same scaling factors at initial of DAFLC. In this concern, to show the visual indications of the control performance, an objective measure of an error performance was made using the root mean square error (RMSE) and the mean absolute error (MAE) criteria [27,28]. The RMSE and the MAE are defined in Eqs. (13) and (14), respectively:

$$MAE = \frac{1}{N} \sum_{i=1}^{N} |e(t)|$$
(13)

$$RMSE = \sqrt{\frac{1}{N} \sum_{i=1}^{N} (e(t))^{2}}$$
(14)

5.1. Task 1: simulation results

5.1.1. Disturbance with 30% and 60% load

During the present part of the work, simulation results for the FLC and DAFLC responses, under 30% and 60% loads, on the DC motor at k = 500 were presented, as shown in Figs. 7 and 8. From this, the RMSE values for the proposed controller and other controllers are shown in Figs. 9 and 10. Besides, the simulation was extended to include the effect of changing the set point of the speed response Fig. 11. It is clearly shown that the DAFLC has better rise time and the priority to respond when applying load rather than FLC.

5.2. Task 2: experimental results

5.2.1. Square wave input for the DC motor

In addition to the simulation, experimental investigation was carried out Fig. 12, where, it is shown that the step response uses a reference speed square wave with positive and negative values (600 rpm and -600 rpm), and it is clear that the proposed DAFLC is better than FLC in the rising time.

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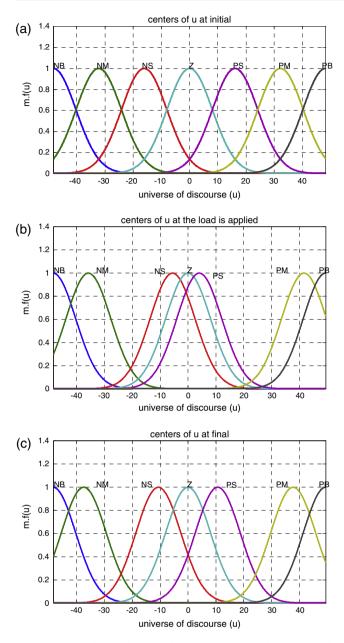


Figure 15 Updates of centers for the DAFLC under 60% load: (a) at initial, (b) at the load and (c) at final.

| Table 3Updates of gains for DAFLC. | | | | | | |
|------------------------------------|-------|------|--------|-------|------|------|
| 30% load | | | 60% lo | ad | | |
| | Gl | G2 | G3 | Gl | G2 | G3 |
| At initial | 6 | 0.6 | 0.8 | 6 | 0.6 | 0.8 |
| At load | 17.78 | 5.31 | 8.34 | 14.98 | 4.7 | 6.43 |
| At final | 17.37 | 5.15 | 8.08 | 14.61 | 4.55 | 6.19 |

5.2.2. External disturbance with 30% and 60% load

The open loop response of the DC motor, FLC with DAFLC response under 30% and 60% load, reference speed (600 rpm), its control signals and current waveform are illustrated as shown in Figs. 13 and 14, where the centers for DAFLC are

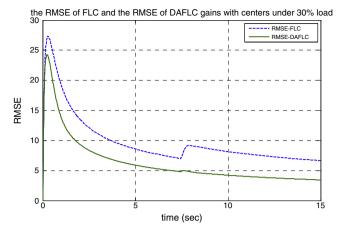


Figure 16 RMSE of FLC with DAFLC at 30% load.

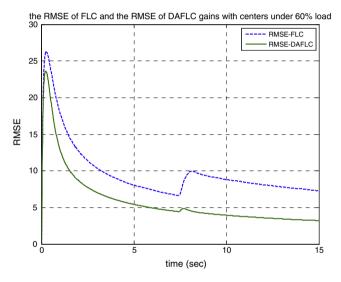


Figure 17 RMSE of FLC with DAFLC at 60% load.

| Table 4 | MAE and | RMSE | for FLC | and DAFLC. |
|---------|---------|------|---------|------------|
|---------|---------|------|---------|------------|

| | At 30% load | | At 60% load | | |
|-------------|-------------|------------------------------|-------------|------------------------------|--|
| | FLC | DAFLC with centers and gains | FLC | DAFLC with centers and gains | |
| RMSE MAE | | 3.4336 0.1691 | | 3.2047 0.1792 | |

adapted as shown in Fig. 15. After the load is applied at instant (7.5 s) the updates of gains values are listed in Table 3. The RMSE values for the proposed controller with FLC at 30% load and 60% load are shown in Figs. 16 and 17. The RMSE and MAE values for FLC and DAFLC are illustrated in Table 4. The presented controllers are implemented in different Arduino boards for computing the computation time and implemented size memory; this is presented in Tables 5 and 6. From experimental results it becomes clear that the proposed DAFLC is better than FLC in terms of performance and the priority to respond when applying the load.

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Table 5 Memory size for FLC and DAFLC with different Arduino boards.

| | Arduino UNO | Arduino MEGA | Arduino DUE |
|----------------|---------------------------------|-------------------|------------------|
| Maximum memory | 32,256 bytes | 258,048 bytes | 524,288 bytes |
| FLC | 53% (17,164 byte) | 7% (19,824 byte) | 7% (38,300 byte) |
| DAFLC | 100% (32,460 byte) over size of | 13% (34,352 byte) | 8% (45,084 byte) |
| | memory cannot be implemented | | |

| Table 6 | Time of | computation | for | FLC | and | DAFLC | with |
|-----------|-----------|-------------|-----|-----|-----|-------|------|
| different | Arduino ł | ooards. | | | | | |
| | A 1 . | | | | 1 . | A 1 | |

| | Arduino UNO | Arduino MEGA | Arduino DUE |
|--------------|---|--------------------|-------------------|
| FLC DAFLC | 2252 μs Over size of memory cannot be implemented | 2352 μs 3824 μs | 989 μs 1246 μs |

6. Conclusions

From the experimental work, results, simulation and discussions, it is proved that the design and implementation of both the FLC and the DAFLC were successfully approached, where precise control of the DC motor speed was reported. The DAFLC was tested under two different load conditions, while monitoring its ability to control the speed in both directions. Both FLC and DAFLC are implemented on Arduino DUE hardware using Arduino 1.5.5-r2 software, which are designed to achieve as mentioned high performance and to eliminate the disturbance load. The simulation and practical results show that the proposed controller is able to respond the disturbance, and it is clear that the proposed DAFLC had better performance than FLC.

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