Physics-Based Analysis and Simulation of 1/f Noise in MOSFETs Under Large-Signal Operation

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Abstract—This paper presents a study on 1/f noise in MOSFETs under large-signal (LS) operation, which is important in CMOS analog and RF integrated circuits. The flicker noise is modeled with noise sources as a perturbation in the semiconductor equations employing McWhorter's oxide-trapping model and Hooge's empirical 1/f noise model. Numerical results are shown for 1/f noise in the MOSFET in both small-signal operation and periodic LS operation. It is shown that McWhorter's model does not give any significant 1/f noise reduction when the oxide traps are distributed uniformly in energy and space. In contrast, Hooge's model gives almost 6-dB 1/f noise reduction as the gate OFF-voltage decreases below the threshold voltage. It is found that both models fall short of explaining the noise reduction by more than 6 dB, as observed experimentally in the literature. However, when only one active oxide trap is considered, which generates random telegraph signal (RTS) in drain current, the LS operation gives more than 6-dB low-frequency RTS noise reduction.

Index Terms—Cyclostationary noise, Hooge's empirical 1/f noise model, McWhorter's oxide-trapping model, 1/f noise in MOSFETs.

I. INTRODUCTION

F OR the analysis and design of CMOS analog and RF integrated circuits, 1/f noise in MOSFETs is one of the factors limiting the achievable dynamic range of electronic MOS circuits. For example, 1/f noise has been known as the major cause of close-in phase noise in CMOS RF oscillators [1].

The analysis and characterization of 1/f noise in MOSFETs is typically performed for MOSFETs in the small-signal operation, where only dc biases are imposed on the device electrodes.

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Experimental results [2]–[4], however, show that the estimates based on the small-signal 1/f noise model can be inaccurate, particularly for the periodically switched MOSFETs. It was shown that the measured power spectral density of the drain noise voltage (or current) is much lower than the estimate using the small-signal 1/f noise model, i.e., 6-dB noise power reduction. Although modeling efforts to explain and predict these observed effects [5], [6] have been made, the physicsbased analysis and simulation of 1/f noise in MOSFETs has been performed only in the small-signal operation regime [7], [8]. To our knowledge, its extension to the periodic large-signal (LS) operation has not been reported yet.

In this paper, we calculate the power spectral density of the drain 1/f noise current in the MOSFET in periodic LS operation by using both the oxide-trapping model [7], [9] and Hooge's model [10]–[12]. This paper is organized as follows: In Section II, we briefly describe the implementation of the physics-based simulation of 1/f noise in the MOSFET using both the oxide-trapping model and Hooge's empirical relation. In Section III, numerical results and their discussions are given for the MOSFET in the periodic LS as well as in the smallsignal operation. The conclusion is given in Section IV.

II. Physics-Based Analysis and Simulation of 1/f Noise in MOSFET

In spite of extensive studies for nearly half a century, the physical origin of 1/f noise in MOSFETs has not been unambiguously understood [12], [13]. However, it is widely accepted that the sources of 1/f noise in MOSFETs are located at or near the Si/SiO₂ interface. At present, there exist two theories on the physical origin of 1/f noise in MOSFETs: 1) the carrier number fluctuation theory based on the oxide-trapping model [7], [9], [14] and 2) the mobility fluctuation theory based on Hooge's well-known empirical relation [10]–[12].

Here, we employ both McWhorter's oxide-trapping noise model and Hooge's empirical 1/f noise model to compare their simulation results on 1/f noise under LS operation as a platform for our simulation study. We choose an NMOSFET as a basis of our discussion, but its application to PMOSFETs is straightforward.

A. McWhorter's Oxide-Trapping Model

Fig. 1 shows the energy band diagram that illustrates possible carrier transition processes at the interface and in the oxide, which is considered for the McWhorter 1/f noise model in this



Fig. 1. Energy band diagram that illustrates possible carrier transition processes at the interface and in the oxide. Only traps with energy E are depicted.

study. Electrons in the conduction band can get trapped into (process a) or detrapped from (process b) the localized defect centers at the interface. The trapped electrons at the interface can also get captured or emitted with defect centers in the oxide that have the same energy through the tunneling mechanism (processes c and d).

Let us consider oxide traps with energy level E, whose volume density is $N_{t,ox}$. $N_{t,int}$ denotes the sheet density of the interface traps that can exchange electrons with the oxide traps with the same energy E. In addition, $n_{t,ox}$ and $n_{t,int}$ represent the trapped electron volume density in the oxide traps and the trapped electron sheet density in the interface traps, respectively. The expressions characterizing the processes a, b, c, and d in Fig. 1 respectively become

$$r_{n,\text{int}} = c_n n (N_{t,\text{int}} - n_{t,\text{int}}) \tag{1}$$

$$g_{n,\text{int}} = c_n n_{t,\text{int}} n_1 \tag{2}$$

$$r_{\rm int,ox} = T n_{t,\rm int} \Delta x (N_{t,\rm ox} - n_{t,\rm ox}) \tag{3}$$

$$g_{\text{int,ox}} = T(N_{t,\text{int}} - n_{t,\text{int}})n_{t,\text{ox}}\Delta x.$$
(4)

Note that the four quantities $r_{n,int}$, $g_{n,int}$, $r_{int,ox}$, and $g_{int,ox}$ have the unit of cm⁻² sec⁻¹. In (1)–(4), n is the electron volume density in the conduction band at the interface, c_n is the capture coefficient for electrons, n_1 is the Shockley density, which is the electron density when the electron quasi-Fermi energy is equal to E, and T is the tunneling coefficient with the unit of cm² sec⁻¹. The electron continuity equation, the interface trap continuity equation, and the oxide trap continuity equation can respectively be written as

$$\int_{\Omega_{\rm int}} d\mathbf{a} \cdot \mathbf{J}_n = q \int_{A_{\rm int}} da(r_{n,\rm int} - g_{n,\rm int}) + q \int_{\Omega_{\rm int}} d\mathbf{r} \frac{\partial}{\partial t} n \quad (5)$$

$$0 = q \int_{A_{\rm int}} da \left(\frac{\partial}{\partial t} n_{t,\rm int} - r_{n,\rm int} + g_{n,\rm int} + r_{\rm int,ox} - g_{\rm int,ox} \right) \quad (6)$$

$$0 = q \int_{\Omega_{\rm ox}} d\mathbf{r} \frac{\partial}{\partial t} n_{t,\rm ox} + q \int da(-r_{\rm int,ox} + g_{\rm int,ox}). \quad (7)$$

 A_{int}



Fig. 2. Spatial discretization of the MOSFET. Ω_{int} is the control volume whose surface includes the interface, A_{int} is the area of the interface, and Ω_{ox} is the control volume of the oxide node where the oxide trap is located.



Fig. 3. Energy band diagram that illustrates the direct transitions between the conduction band and the oxide traps under the quasi-static assumption of $n_{t,\text{int}}$. We discretize the selected energy range in the oxide bandgap in small discrete energy interval ΔE .

In (5)–(7), Ω_{int} and Ω_{ox} are the control volumes, and A_{int} is the interface area, as shown in Fig. 2.

Since the trap centers at the interface and in the oxide are distributed in energy and space, discretization in both variables is needed. In the previous study on the physics-based simulation of 1/f noise in MOSFETs [7], only the trap levels at the electron quasi-Fermi level were discretized, mainly due to the computational efficiency. This approach can be valid only when the electron quasi-Fermi level of the oxide traps is aligned with that of the interface traps. However, when the MOSFET is switched periodically, this assumption is no longer valid, as shown in the following section, because the electron trapping and detrapping by the oxide traps, which are related with 1/f noise in the oxide-trapping model, are known to be slow processes compared with the switching frequency.

Therefore, in this paper, we discretize the selected energy range in the oxide bandgap with small discrete energy interval ΔE to account for the trap states that are related with 1/f noise under LS excitation condition, as shown in Fig. 3. Although this implementation is computationally expensive, we expect that it gives more accurate results. Instead, we make the following assumption to reduce the computational complexity. Since the time constant for the interface trap is much shorter than that of the oxide trap [7], [9], [15], the quasistatic approximation on $n_{t,int}$ can be made as

$$\frac{\partial}{\partial t}n_{t,\text{int}} = 0. \tag{8}$$

Through this simplification, we can consider the transitions of electrons between the conduction band and the oxide traps to be direct without the intermediate interface states

$$\int_{\Omega_{\rm int}} d\mathbf{a} \cdot \mathbf{J}_n = q \int_{A_{\rm int}} da (r_{\rm int,ox} - g_{\rm int,ox}) + q \int_{\Omega_{\rm int}} d\mathbf{r} \frac{\partial}{\partial t} n + q\gamma$$
(9)
$$\mathbf{0} = q \int_{\Omega_{\rm ox}} d\mathbf{r} \frac{\partial}{\partial t} n_{t,ox} + q \int_{A_{\rm int}} da (-r_{\rm int,ox} + g_{\rm int,ox}) - q\gamma.$$
(10)

Since we calculate the self-consistent solution of Poisson's equation and the continuity equations, the effect of the trapped electron in the oxide trap on the electrons in the channel is self-consistently included in our simulation results. Note that the nonlocal white noise source γ appears in the above continuity equations. Its correlation function can be expressed as follows:

$$R_{\gamma,\gamma}(t,t') = (r_{\text{int,ox}} + g_{\text{int,ox}})\delta(t-t').$$
(11)

The relevant transfer functions for this noise source are Green's functions for the oxide trap continuity equation and the electron continuity equation.

B. Hooge's Empirical Relation

Hooge's empirical relation [10]-[12] reads

$$S_{I,I} = \frac{\alpha_H}{f} \frac{I^2}{N} \tag{12}$$

where all the physical mechanisms related with the noise are contained in Hooge's parameter α_H . A phenomenological local colored (frequency-dependent) noise source $\boldsymbol{\xi}_n$ is superimposed in the current density. The continuity equation for the conduction band electron can be written as

$$\nabla \cdot (\mathbf{J}_n + \boldsymbol{\xi}_n) = q \frac{\partial n}{\partial t}.$$
 (13)

The relevant transfer function for Hooge's noise source is the gradient of Green's function for the electron continuity equation. The power spectral density of Hooge's noise source can be written as

$$S_{\xi_n,\xi_n}(\mathbf{r},\mathbf{r}',f) = \frac{\alpha_H(\mathbf{r})}{f} \frac{\left|\mathbf{J}_{ns}(\mathbf{r})\right|^2}{n(\mathbf{r})} \delta(\mathbf{r}-\mathbf{r}')I \qquad (14)$$

where \mathbf{J}_{ns} is the steady-state electron current density, and I is a unit tensor.

Since Hooge's noise source is colored, its extension to the LS operation regime is not straightforward. We assume that Hooge's parameter is an instantaneous function. According to the scheme where frequency modulation is followed by amplitude modulation [16], we can find that the diagonal component

of the sideband correlation matrix [17]–[19] for Hooge's noise source is given by

$$S_{\xi_n,\xi_n}(\mathbf{r},\mathbf{r}',mf_0+f,mf_0+f) = |K_m(\mathbf{r})|^2 \frac{1}{f} \delta(\mathbf{r}-\mathbf{r}')I$$
(15)
where $K_m(\mathbf{r})$ is the *m*th Fourier coefficient of $\alpha_H(\mathbf{r})(|\mathbf{J}_{ns}(\mathbf{r})|^2/n(\mathbf{r})).$

III. NUMERICAL RESULTS

Both McWhorter's oxide-trapping model and Hooge's empirical model, described in the previous section, have been implemented in our in-house device-circuit mixed-mode simulator Circuit LEvel SImulation COde [20], [21].

To perform the physics-based noise simulation, we should first find the steady-state solutions-dc solutions or periodic LS solutions-without the influence of the noise sources. In the case of small-signal noise simulation, the dc solutions can be obtained easily by using the conventional dc simulation. In the case of the LS noise simulation, the periodic LS solutions can be obtained by using the periodic steady-state analysis methods, such as the finite-difference method [22], [23], the harmonic balance method [24], [25], or the shooting-Newton method [21], [26]. In this paper, the harmonic balance method has been exploited to be consistent with the following noise simulation. For the LS noise simulation, we employ the conversion Green's function technique that extends Green's function method for steady-state noise calculation into periodic steadystate noise calculation [17], [19], [20], [27], [28]. In the case of the LS analysis, the number of unknown variables used in the numerical procedure is significantly increased with the number of harmonics (or the number of discretized time points) considered in the simulation when compared with that in the case of small-signal analysis. Such a large linearized system equation is solved with ILUPACK, which is an efficient sparse matrix solver [29].

To illustrate the main differences that originated from two noise models, a long-channel MOSFET structure is chosen as the device under test. For such a device, a clear 1/f noise reduction by more than 6 dB has been observed experimentally under the switched operation with 50% duty cycle [2]–[4]. Application to more scaled devices can be an interesting topic and will be dealt with elsewhere.

A. Small-Signal Operation With Oxide-Trapping Model

We first calculate the 1/f noise spectrum using the oxidetrapping model when the MOSFET is in the small-signal operation.

Fig. 4 shows the cross-sectional view of the MOSFET under simulation. The gate contact is located from $-1.0 \,\mu$ m to $1.0 \,\mu$ m in the lateral position. The Si-SiO₂ interface is located at zero point in the vertical position. The thickness of the gate oxide is 9.0 nm. The threshold voltage of the MOSFET is calculated to be about 0.5 V. We consider the oxide trap levels beneath the gate contact, which are spatially located within 2.0 nm from the interface. The device width is 1.0 μ m. The unified



Fig. 4. Cross-sectional view of the MOSFET under simulation. The gate contact is located from -1.0 to $1.0 \ \mu m$ in the lateral position. The zero point in the vertical position is the oxide–silicon interface. The thickness of the gate oxide is 9.0 nm. The threshold voltage of the MOSFET is 0.5 V. The "oxide trap region" box indicates the region where the oxide trap levels are discretized. The device width is $1.0 \ \mu m$.

mobility model proposed by Darwish *et al.* [30] is adopted, and its dependence on the electric field is correctly considered.

The oxide trap density per unit volume and per unit energy at the trap energy E_t , i.e., $N_{t,ox}(E_t)$, may be modeled as a U-shaped distribution [31], [32] as

$$N_{t,\text{ox}}(E_t) = N_0 \exp\left[\eta \times (E_t - E_i)\right]$$
(16)

where N_0 and η are the fitting parameters, and E_i is the intrinsic energy level of silicon. The modeling of the oxide trap density has a significant impact on the noise simulation result. For example, in [33], the 1/f noise reduction under the switched operation observed in the experiments is attributed to this U-shaped distribution. In this paper, we consider two parameter sets: one is the usual U-shaped distribution taken from the literature [7], [31], [32], whereas the other is the uniform distribution originally assumed in McWhorter's oxide-trapping model. The actual values used in the simulations are $N_0 = 4 \times 10^{17} \text{ cm}^{-3} \text{ eV}^{-1}$ and $\eta = 3.1 \text{ eV}^{-1}$ for the U-shaped distribution and $N_0 = 4 \times 10^{17} \text{ cm}^{-3} \text{ eV}^{-1}$ and $\eta = 0$ for the uniform distribution.

We consider oxide trap levels whose E_t values, measured from the electron quasi-Fermi level at the reference bias condition, are from -0.45 to 0.15 eV. The electron quasi-Fermi level is self-consistently determined under the presence of oxide trap levels. For that energy range, we introduce 25 discretized energy levels with the energy interval $\Delta E = 25$ meV. Since there are 600 spatial nodes in the "oxide trap region" in Fig. 4, we need 15 000 additional unknown variables for the oxide traps. After the trap energy is determined self-consistently with the electron quasi-Fermi level at the reference bias condition, i.e., when the MOSFET is under the LS operation, the relative position in the energy band diagram of an oxide trap depends on the bias condition.

The tunneling coefficient T is given by

$$T = T_0 \exp\left(-\alpha |x|\right) \tag{17}$$



Fig. 5. Simulated power spectral densities of the drain noise current at the gate voltage 1.0 V as a function of frequency. The drain voltage is fixed to be 10 mV. The flat lines indicate the power spectral densities due to the diffusion noise sources.



Fig. 6. Simulated power spectral densities of the drain noise current at 10 Hz as a function of gate voltage. Two different distributions of the oxide trap are considered. The drain voltage is fixed to be 10 mV. When the gate voltage is below the threshold voltage, which is 0.5 V for this simulation, the power spectral density of the drain noise current is much smaller than that of strong inversion.

where x is the depth of the oxide trap from the interface, T_0 is set to be 1.0 cm² sec⁻¹, and the attenuation constant α is calculated using the trapezoidal barrier approximation. Therefore, the Jacobian matrix contains nonlocal components. Note that the validity of the simple square barrier approximation is doubtful under the LS operation since the barrier height depends on the bias condition. The interface trap density per unit energy is set to be 10^{11} cm⁻² eV⁻¹.

Fig. 5 shows the simulated power spectral densities of the drain noise current at the gate voltage of 1.0 V as a function of frequency. The drain voltage is fixed to be 10 mV. As expected, the oxide-trapping noise shows a clean 1/f noise spectrum. The change of slope due to the nonuniform trap distribution is negligible in the simulation. Fig. 6 shows the simulated power spectral densities of the drain noise current at 10 Hz as a function of gate voltage. When the gate voltage is below the threshold voltage, i.e., 0.5 V for this simulation, the power



Fig. 7. Contribution of each trap with different energy to the power spectral density of the drain noise current at the gate voltage of 1.0 V. The drain voltage is fixed to be 10 mV. Uniform trap density in energy and space is assumed. The distribution width of the noise contribution is about $4k_BT \approx 0.1$ eV.



Fig. 8. Drain current Green's function for the oxide trap continuity equation in the "oxide trap region" at the gate voltage of 1.0 V and at 10 Hz. For this trap level, the energy difference from the electron quasi-Fermi energy is zero. The drain voltage is fixed to be 10 mV. The Si-SiO₂ interface is located at the zero vertical position. The value for the uniform trap distribution is shown.

spectral density of the drain noise current shows a very small value. The difference of the noise level between two parameter sets, which is prominent in the higher $V_{\rm GS}$ region, can be understood from the difference of the oxide trap density near the conduction band.

In Fig. 7, the contribution of each trap to the drain noise is shown. The trap levels whose E_t 's are close to the electron quasi-Fermi level contribute to the drain noise significantly. Therefore, for the small-signal noise analysis, it can be justified to consider the trap levels around the electron quasi-Fermi level only. Fig. 8 shows the drain current Green's function for the oxide trap continuity equation in the "oxide trap region." The gate voltage is 1.0 V, the drain voltage is 10 mV, and the observation frequency is 10 Hz. In Fig. 8, the oxide traps whose energy levels in the energy band diagram coincide with the electron quasi-Fermi energy at the interface are chosen. Both the noise source [the integrated γ in (9) and (10)] and the observation quantity (the terminal noise current) have the same



Fig. 9. Square-wave signal with 50% duty cycle applied to the gate terminal. The frequency of the square-wave signal is 10 kHz. The ON voltage is 1.0 V, and the OFF voltage is changed from 0.9 to 0.1 V with 0.1-V interval. The drain voltage is fixed to be 10 mV.

unit; therefore, the terminal current Green's function is given by a unitless quantity. While the drain current Green's function for the oxide trap continuity shows a large magnitude due to the amplification effect (for example, on the order of 10^6 in Fig. 8), usually its magnitude for electron continuity is smaller than unity. Since their magnitudes show a huge difference, only the Green's function for the oxide trap continuity equation is important in the case of the oxide-trapping model, although both transfer functions are considered in our simulation. The real part of Green's function represents the drain noise current whose phase is the same as g-r noise sources in the oxide traps.

B. LS Operation With Oxide-Trapping Model

Now we calculate the 1/f noise spectrum when the MOS-FET is in the LS operation. We consider the simplest case of a periodically switched MOSFET. Since our aim is to simulate the experimental setup [2]–[4], the gate voltage is driven by a square-wave signal with 50% duty cycle, as shown in Fig. 9. The frequency of the square-wave signal is 10 kHz. The ON voltage is 1.0 V, and the OFF voltage is varied from 1.0 (dc) to 0.1 V. The transition regions between the ON and OFF voltages are introduced to avoid problems with harmonic balance simulation. The drain bias voltage is fixed to be 10 mV. The harmonic balance simulation for the periodic steady state is carried out including 24 harmonics plus dc, thus leading to 49 collocation points.

Since full noise analysis is performed, any (m, m') component of the sideband correlation matrix $S(mf_0 + f, m'f_0 + f)$ [17], [19] is available for the given sideband frequency f. Among the many components of the sideband correlation matrix, usually, the diagonal components correspond to the noise power spectral density measured in the experiments. In Fig. 10, the diagonal components of the sideband correlation matrix of the drain noise current are plotted up to the first harmonic. It is clearly seen that a certain amount of the oxide-trapping noise is up-converted from the baseband to the first harmonic. Since the baseband noise is of particular interest in these experiments



Fig. 10. Power spectral density up to the first harmonic. Simulated result for the U-shaped distribution is shown for the OFF voltage of 0.5 V. The center frequency of the first harmonic is 10 kHz.



Fig. 11. Noise reduction factor for different OFF voltages. The small-signal 1/f noise model predicts 6-dB noise reduction.

[2]–[4], in the following discussion, we concentrate on the noise characteristics in the baseband.

When the OFF voltage is $v_{\rm off}$ V, we define the noise reduction factor Γ_v as

$$\Gamma_v = \frac{S_{I_D, I_D}(\mathsf{dc})}{S_{I_D, I_D}(\mathsf{OFF} = v_{\mathrm{off}})}.$$
(18)

Since the small-signal 1/f noise model predicts 6-dB noise reduction, the noise reduction factor should be 4. Fig. 11 shows the simulated noise reduction factor for different OFF voltages. When the trap distribution is uniform, the reduction factor of the drain noise current is close to unity. Its maximum value is at most 1.20 for the OFF voltage of 0.4 V. This is in strong contrast with the experimental results [2]–[4]. When the trap distribution is U-shaped, the reduction factor is slightly increased for all OFF voltages. However, its effect is not significant, although we adopt a reasonable value for the trap distribution. Therefore, the nonuniform distribution of the oxide trap in the energy space cannot explain the noise reduction over 6 dB properly, at least in this simulation.



Fig. 12. Drain current conversion Green's functions for the oxide trap continuity equation in the "oxide trap region" at 10 Hz. The OFF voltage is 0.1 V.



Fig. 13. Trap occupancy at 0.05 ms when the OFF voltage is 1.0 (dc) or 0.1 V. The trapped electron density in the oxide trap is reduced for the lower OFF voltages.

To explain this result, the behavior of the internal quantities is investigated. In Fig. 12, the conversion Green's functions from baseband to baseband for the oxide trap continuity equation are shown for the dc case and the OFF voltage of 0.1 V. Since both trap distributions show quite similar results, the values for uniform distribution are shown. Among the 25 trap levels included in this simulation, the one that is exactly aligned with the electron quasi-Fermi level at the dc bias condition ($V_{\rm GS} =$ 1 V and $V_{\rm DS} = 10$ mV) is chosen. In other words, this trap level is the biggest contributor to the drain noise current for the dc case. The conversion Green's function for the OFF voltage of 0.1 V is half of that for the dc case. Since the square of the conversion Green's function is involved with the output power spectral density, it means that a 6-dB noise reduction will be observed due to the reduced transfer function when we consider only this trap level. In Fig. 13, the trap occupancy of the same trap level at 0.05 ms is shown for the dc case and the OFF voltage of 0.1 V. Traps close to the interface (up to -0.8 nm in the figure) are almost empty because they can easily exchange electrons with the conduction band. Even in the case of traps



Fig. 14. Contribution to the power spectral density at 10 Hz from each trap level. Normalized values to the dc ones are shown.

located far from the interface, the trapped electron density in the oxide trap is significantly reduced for lower OFF voltage. Then, the power spectral density of the Langevin noise source is also reduced. Therefore, an additional noise reduction of over 6 dB will be observed due to the reduced trap occupancy when we consider only this trap level.

However, when we consider all trap levels, the above discussion is no longer valid. In Fig. 14, the contribution to the power spectral density from each trap level is shown. In the dc case, only the traps near the electron quasi-Fermi level contribute dominantly. However, in the switching case, the contribution is spread out over a wide range. In particular, traps that are fully filled in the dc case contribute significantly in the switching case. Under the LS operation, they are no longer fully occupied, so the transition between these trap levels and the conduction band is increased.

C. LS Operation With Hooge's Empirical Model

Using Hooge's empirical model, the same device has been simulated. At this time, the MOSFET is simulated without the oxide traps. For the sake of simplicity, we employ a constant Hooge's parameter 2.4×10^{-4} , which is found to generate the same noise power at the reference bias condition ($V_{\rm GS} =$ 1.0 V). Fig. 15 shows the noise reduction factor for different OFF voltages. Although the reduction factor of Hooge's model is larger than that of the oxide-trapping model, the additional noise reduction over the conventional 6-dB noise reduction is not observed even in this case either.

In Fig. 16, the conversion Green's functions from baseband to baseband for the electron continuity equation are shown for the dc case and the OFF voltage of 0.1 V. The relevant transfer function is the gradient of the conversion Green's function for the electron continuity equation. The gradient of the Green's function is slightly larger in the LS case than in the small-signal case. A slight decrease of the simulated reduction factor from the ideal value of 4 can be attributed to this change of relevant transfer function. Therefore, the modulation of Hooge's noise source plays an important role in 1/f noise reduction in this model.



Fig. 15. Noise reduction factor for different OFF voltages when Hooge's empirical model is exploited. Hooge's parameter is assumed to be instantaneous.



Fig. 16. Drain current conversion Green's function from baseband to baseband for the electron continuity equation. The drain voltage is 10 mV. Values at Si-SiO₂ are shown.

Hooge's noise source for switched MOSFET can be analyzed as follows. For the baseband frequency, the power spectral density of Hooge's noise source in (15) becomes

$$S_{\xi_n,\xi_n}(\mathbf{r},\mathbf{r}',f,f) = |K_0(\mathbf{r})|^2 \frac{1}{f} \delta(\mathbf{r}-\mathbf{r}')I \qquad (19)$$

where K_0 is the time average of the amplitude modulation over one period T_0 , i.e.,

$$K_0 = \frac{1}{T_0} \int_0^{T_0} \alpha_H(\mathbf{r}) \frac{|\mathbf{J}_{ns}(\mathbf{r})|^2}{n(\mathbf{r})} dt.$$
 (20)

Since the current density almost vanishes in the OFF condition, if the gate voltage is driven by a square-wave signal with 50% duty cycle, then the expression for K_0 is simplified to

$$K_0 = \frac{1}{2} \alpha_{H,\text{on}}(\mathbf{r}) \frac{|\mathbf{J}_{ns,\text{on}}(\mathbf{r})|^2}{n_{\text{on}}(\mathbf{r})}$$
(21)

where the subscript *on* denotes the quantity at the ON condition. Therefore, the power spectral density of Hooge's noise source is reduced by 6 dB. Note that Hooge's parameter in the OFF condition does not appear in the above expression. From this argument, we can conclude that Hooge's parameter for the MOSFET cannot be an instantaneous function to explain the experimental results of more than 6-dB noise reduction. Rather, it should contain information about the periodic device operation.

IV. CONCLUSION

The 1/f noise in MOSFETs under LS operation has been modeled by using both McWhorter's oxide-trapping model and Hooge's empirical model. In the oxide-trapping model, the contribution to noise power spectral density (PSD) of traps aligned to the electron quasi-Fermi level at the dc case was decreased by more than 6 dB. However, when the trap distribution was uniform, no significant reduction of the 1/f noise PSD under LS operation was observed. When the trap distribution was U-shaped, the noise reduction factor was increased, but its effect was not significant. In Hooge's model, an almost 6-dB noise decrease was observed for the low OFF voltages. It is the same result with that of the small-signal 1/f noise model. When Hooge's parameter for the MOSFET was an instantaneous function, we could not explain the additional decrease of noise. To explain the experimental results using this model, the noise source should depend not only on the instantaneous operating point but also on the periodic device operation over the whole period. In idealized cases, both models fall short of predicting more than 6-dB decrease of 1/f noise PSD in MOSFETs under the LS condition.

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